

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Chen et al.

Title: Read and Erase Verify Methods and Circuits Suitable for Low Voltage Non-Volatile Memories

Application No.: 10/552,948 Filing Date: April 8, 2004

Examiner: Unknown Group Art Unit: Unknown

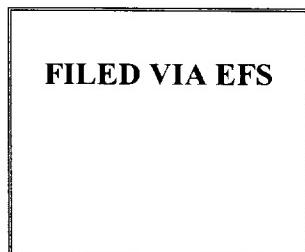
Docket No.: SNDK.284US1 Conf. No.: 6753

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR EXTENSION OF TIME

Dear Sir:

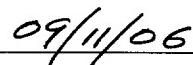
Applicants respectfully petition for a two-month extension of time within which to respond to the outstanding Notice to File Missing Requirements Under 35 USC 371, such extension allowing the undersigned until October 6, 2006 to respond. The fee of \$450.00 has been authorized via EFS to Deposit Account 502664. The Commissioner is hereby authorized to charge any additional fees, which may be required, or credit any overpayment to Deposit Account 502664.



Respectfully submitted,


Gerald P. Parsons
Reg. No. 24,486

Date


09/11/06

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